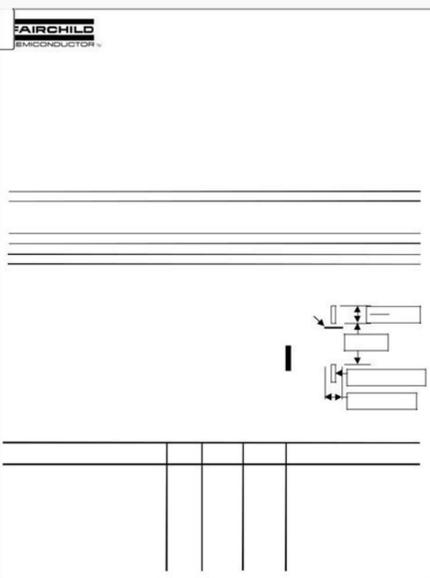




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1.5KE6.8A thru 1.5KE540A, 1N6267A thru 1N6303A
 Vishay General Semiconductor

TRANSZORB® Transient Voltage Suppressors

FEATURES

- Glass passivated chip junction
- Available in unidirectional and bi-directional
- 1500 pF peak pulse power capacity with a 10/1000-µs waveform, relative rise/fall time 0.01 %
- Excellent clamping capability
- Very fast response time
- Low inductance surge resistance
- AEC-Q101 qualified
- Solderable 275 °C max. to lead, JEDEC 22-B106
- Material categorization: for definitions of compliance please see www.vishay.com/doc?91002

PRIMARY CHARACTERISTICS

Reverse-directional	6.8 kV to 540 V
Bi-directional	6.8 kV to 440 V
Power	1000 W
P_{SM}	4.5 W
Peak浪涌 current	200 A
T_{max}	275 °C

TYPICAL APPLICATIONS

Use in sensitive electronics protection against voltage transients induced by inductive load switching and lightning on CL, MODREF, signal lines or sensor units for consumer, automotive, industrial, aerospace, and telecommunications.

MECHANICAL DATA

Case: molded epoxy body over passivated junction making compound meets UL 94 V-0 flammability rating. Base Film E3 - RoHS compliant, commercial grade. Base Film H3ES - RoHS compliant, AEC-Q101 qualified.

Terminal: Matte Sn plated leads, solderable per JEDEC-002 and JEDEC 22-B106. E3 suffix meets JEDEC 0201 class 1A, surface test, HES suffix meets JEDEC 0201 class 2 surface test.

Note: 1. 1.5KE6.8A to 1.5KE540A and 1.5KE267A to 1.5KE303A are 1908WCA for commercial grade only.

Power: For unidirectional types the cathode is the device cathode and, for bi-directional types.

MAXIMUM RATINGS ($T_A = 25\text{ °C}$ unless otherwise noted)

PARAMETER	SYMBOL	VALUE	UNIT
Peak pulse power dissipation with a 10/1000-µs waveform**	P_{SM}	1500	W
Peak pulse current with a 10/1000-µs waveform**	I_{SM}	See note 1A	A
Power dissipation at steady state at $T_A = 25\text{ °C}$, Fig. 2	P_D	4.5	W
Peak surge current (10/1000-µs pulse half-wave sine unidirectional only)	I_{SM}	200	A
Maximum instantaneous forward voltage at 100 A for unidirectional only**	V_{SM}	350.0	V
Operating junction and storage temperature range	T_J, T_{STG}	-55 to 175	°C

NOTES

(*) High-reliability units (suffix code 1) are tested above $T_A = 25\text{ °C}$ per Fig. 2.
 (**) Maximum of 0.5 ms surge half-wave sine or equivalent square wave duty cycle at 4 pulses per minute maximum.
 (*** $V_{SM} = 3.5\text{ V}$ for 1.5KE267A and below; $V_{SM} = 50\text{ V}$ for 1.5KE303A and above.

Document Number: 91002-1
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1 AMP GENERAL PURPOSE SILICON DIODES

FEATURES

- PROMOTES BATTERY SOFT CLAMPING FUNCTION
- PASSENGER FOR SUPERIOR RELIABILITY AND PERFORMANCE
- VOID FREE BONDING ON SOLDERING FOR MAXIMUM MECHANICAL STRENGTH AND HEAT DISSIPATION (Solder Wets: Typical $\geq 2\%$, Min $\geq 80\%$ of the Area)
- EXTREMELY LOW LEAKAGE AT HIGH TEMPERATURES
- LOW FORWARD VOLTAGE DROP
- I_A at $T_A = 25\text{ °C}$ WITH NO THERMAL RISEBACK

MECHANICAL DATA

- Case: JEDEC DO-41, 2042 and 2043 (see JEDEC Reference Series M16)
- Terminal: Tin plated wire
- Soldering: See note 1
- Polarity: Cathode band on cathode
- Shipping Position: Flat
- Weight: 0.018 Grams (0.64 Ounces)

MAXIMUM RATINGS & ELECTRICAL CHARACTERISTICS

PARAMETER (TEST CONDITIONS)	SYMBOL	MINIMUM	TYPICAL	MAXIMUM	UNIT
Maximum Reverse Voltage	V_R	50	50	50	V
Maximum RMS Voltage	V_{RMS}	35	35	35	V
Maximum Peak Reverse Voltage	V_{RM}	50	50	50	V
Average Forward Current (Duty Cycle $\leq 10\%$, Lead length $\leq 1.275\text{ m}$, 10 cycles)	I_{AV}	1	1	1	A
Peak Forward Surge Current (10 cycles, single half-wave sine wave, JEDEC Reference Series M16)	I_{FSM}	20	20	20	A
Maximum Forward Voltage at 1 Amp DC	V_F	0.7	0.7	0.7	V
Maximum Reverse Leakage Current (at $T_A = 25\text{ °C}$)	I_{R}	0.1	0.1	0.1	μA
Maximum Reverse Leakage Current (at $T_A = 100\text{ °C}$)	I_{R}	0.1	0.1	0.1	μA
Typical Thermal Resistance (junction to lead)	θ_{JL}	0.1	0.1	0.1	$^{\circ}\text{C}/\text{W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to 175	-55 to 175	175	°C

1 AMP HIGH RELIABILITY SILICON DIODES

FEATURES

- PROMOTES BATTERY SOFT CLAMPING FUNCTION
- PASSENGER FOR SUPERIOR RELIABILITY AND PERFORMANCE
- VOID FREE BONDING ON SOLDERING FOR MAXIMUM MECHANICAL STRENGTH AND HEAT DISSIPATION (Solder Wets: Typical $\geq 2\%$, Min $\geq 80\%$ of the Area)
- EXTREMELY LOW LEAKAGE AT HIGH TEMPERATURES
- LOW FORWARD VOLTAGE DROP
- I_A at $T_A = 25\text{ °C}$ WITH NO THERMAL RISEBACK

MECHANICAL DATA

- Case: JEDEC DO-41, 2042 and 2043 (see JEDEC Reference Series M16)
- Terminal: Tin plated wire
- Soldering: See note 1
- Polarity: Cathode band on cathode
- Shipping Position: Flat
- Weight: 0.018 Grams (0.64 Ounces)

MAXIMUM RATINGS & ELECTRICAL CHARACTERISTICS

PARAMETER (TEST CONDITIONS)	SYMBOL	MINIMUM	TYPICAL	MAXIMUM	UNIT
Maximum Reverse Voltage	V_R	50	50	50	V
Maximum RMS Voltage	V_{RMS}	35	35	35	V
Maximum Peak Reverse Voltage	V_{RM}	50	50	50	V
Average Forward Current (Duty Cycle $\leq 10\%$, Lead length $\leq 1.275\text{ m}$, 10 cycles)	I_{AV}	1	1	1	A
Peak Forward Surge Current (10 cycles, single half-wave sine wave, JEDEC Reference Series M16)	I_{FSM}	20	20	20	A
Maximum Forward Voltage at 1 Amp DC	V_F	0.7	0.7	0.7	V
Maximum Reverse Leakage Current (at $T_A = 25\text{ °C}$)	I_{R}	0.1	0.1	0.1	μA
Maximum Reverse Leakage Current (at $T_A = 100\text{ °C}$)	I_{R}	0.1	0.1	0.1	μA
Typical Thermal Resistance (junction to lead)	θ_{JL}	0.1	0.1	0.1	$^{\circ}\text{C}/\text{W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to 175	-55 to 175	175	°C



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